

● General Description

Through advanced trench and field stop technology to provide very low $V_{CE(sat)}$, low gate charge, and excellent switching performance.

● Features

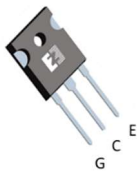
- Very low $V_{CE(sat)}$
- Low switching power loss
- Low switching surge and noise
- Low thermal resistance
- High short circuit capability (10us)
- AEC-Q101 qualified

● Application

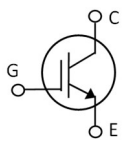
- PTC Heater



● Product Summary



TO-247


 $V_{CE}=1200V$
 $V_{CE(sat)}=1.75V$
 $I_C=80A$


● Ordering Information

Part NO.	ZMBGA80N120S1AC
Marking	BG80N120S1A
Packing information	BULK TUBE
Basic ordering unit (pcs)	600

● Absolute Maximum Ratings ($T_A=25^{\circ}C$, unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Max.	Unit
Collector-emitter voltage	V_{CE}		-	1200	V
Gate-emitter voltage ^①	V_{GE}		-30	30	V
Continuous collector current	I_C	$V_{GE}=15V, T_C=25^{\circ}C$	-	160	A
	I_C	$V_{GE}=15V, T_C=100^{\circ}C$	-	80	A
Pulsed collector current ^①	I_{CM}	Pulsed; $t_p \leq 10 \mu s; T_C = 25^{\circ}C$;	-	320	A
Total power dissipation	P_D	$T_C=25^{\circ}C$	-	833	W
Total power dissipation	P_D	$T_A=25^{\circ}C$	-	3.8	W
Short circuit withstand time	T_{sc}	$V_{GE}=15 V, V_{CE}=600 V, T_J=150^{\circ}C$	-	10	us
Operating junction temperature	T_J		-55	175	$^{\circ}C$
Storage temperature	T_{STG}		-55	175	$^{\circ}C$

● Thermal resistance

Parameter	Symbol	Min.	Typ.	Max.	Unit
Thermal resistance, junction - case	R_{thJC}	-	-	0.18	°C/W
Thermal resistance, junction - ambient	R_{thJA}	-	-	40	°C/W
Soldering temperature (total time<10s)	T_{sold}	-	-	260	°C

● Electronic Characteristics ($T_j=25^{\circ}\text{C}$, unless otherwise specified)

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Collector-emitter breakdown voltage	BV_{CES}	$V_{GE}=0V, I_C=250\mu A$	1200	-	-	V
Gate-emitter threshold voltage	$V_{GE(th)}$	$V_{GE}=V_{CE}, I_C=1.5mA$	4.2	5.2	6.2	V
Zero gate voltage collector current	I_{CES}	$V_{GE}=0V, V_{CE}=1200V, T_j=25^{\circ}\text{C}$	-	-	10	μA
		$V_{GE}=0V, V_{CE}=1200V, T_j=175^{\circ}\text{C}$	-	-	10	mA
Gate- emitter leakage current	I_{GES}	$V_{GE}=\pm 30V, V_{CE}=0V$	-	-	100	nA
Collector-emitter saturation voltage	$V_{CE(sat)}$	$V_{GE}=15V, I_C=80A, T_j=25^{\circ}\text{C}$	-	1.75	2.5	V
		$V_{GE}=15V, I_D=80A, T_j=175^{\circ}\text{C}$	-	2.5	-	V
Forward transconductance	g_{FS}	$V_{CE}=20V, I_C=80A$	-	45	-	S

● Dynamic characteristics ($T_j=25^{\circ}\text{C}$, unless otherwise specified)

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Input capacitance	C_{ies}	$f=100\text{KHz}, V_{CE}=600V, V_{GE}=0V$	-	11628	-	pF
Output capacitance	C_{oes}		-	47	-	pF
Reverse transfer capacitance	C_{res}		-	19	-	pF
Gate resistance	R_g	$f=1\text{MHz}$	-	0.2	-	Ω
Total gate charge	Q_g	$V_{CC}=600V, I_C=80A, V_{GE}=15V$	-	372	-	nC
Gate-emitter charge	Q_{ge}		-	94.6	-	nC
Gate-collector charge	Q_{gc}		-	142.4	-	nC

● Switching characteristics ($T_j=25^{\circ}\text{C}$)

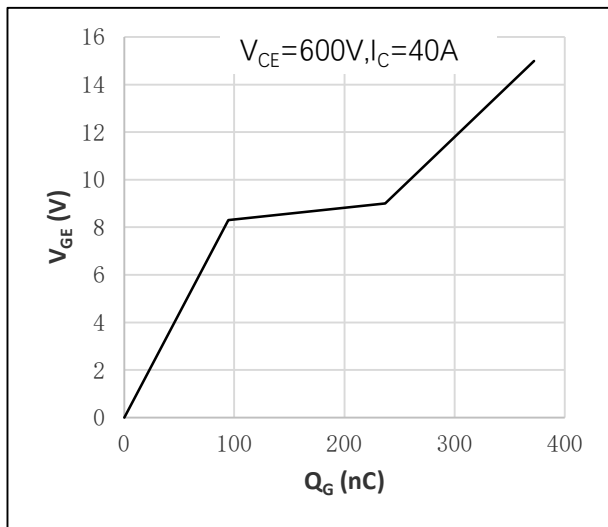
Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Turn-on delay time	$t_{d(on)}$	$T_j=25^{\circ}\text{C}$ $V_{GE}=15/0V,$ $V_{CE}=600V,$	-	105	-	ns
Turn-on rise time	t_r		-	170	-	ns
Turn-off delay time	$t_{d(off)}$		-	231	-	ns

Turn-off fall time	t_f	$R_G = 10\Omega$, $I_C = 80A$, $L = 100\mu H$	-	12	-	ns
Turn-on energy	E_{on}		-	6.69	-	mJ
Turn-off energy	E_{off}		-	4.51	-	mJ
Total switching energy	E_{ts}		-	11.2	-	mJ
Reverse Bias Safe Operating Area	RBSOA	$T_J = 25^\circ C, V_{CC} = 1200V$, $I_C = 320A$, $V_{GE} = 15V/0V, R_g = 10\Omega$	Full Square			-

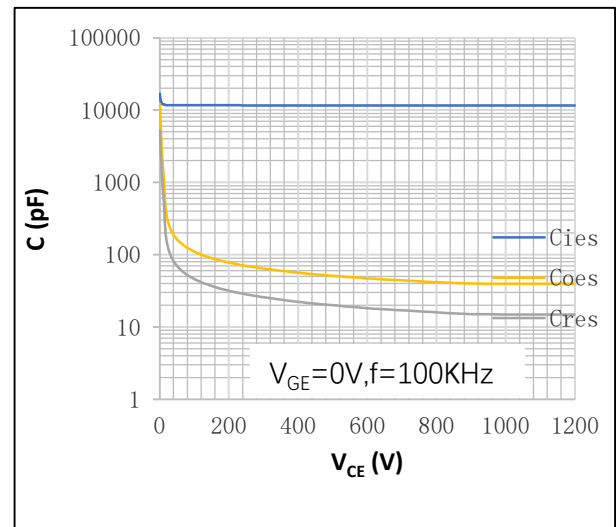
● Switching characteristics ($T_J = 175^\circ C$)

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Turn-on delay time	$t_{d(on)}$	$T_J = 175^\circ C$ $V_{GE} = 15/0V$, $V_{CE} = 600V$, $R_G = 10\Omega$, $I_C = 80A$, $L = 100\mu H$	-	98	-	ns
Turn-on rise time	t_r		-	174	-	ns
Turn-off delay time	$t_{d(off)}$		-	249	-	ns
Turn-off fall time	t_f		-	245	-	ns
Turn-on energy	E_{on}		-	7.49	-	mJ
Turn-off energy	E_{off}		-	5.48	-	mJ
Total switching energy	E_{ts}		-	12.97	-	mJ

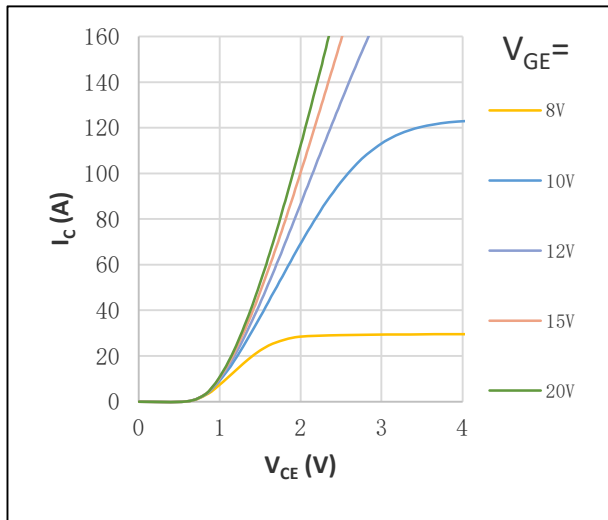
● Fig.1 Gate-emitter voltage as a function of gate charge; Typical values; $T_j=25^\circ\text{C}$



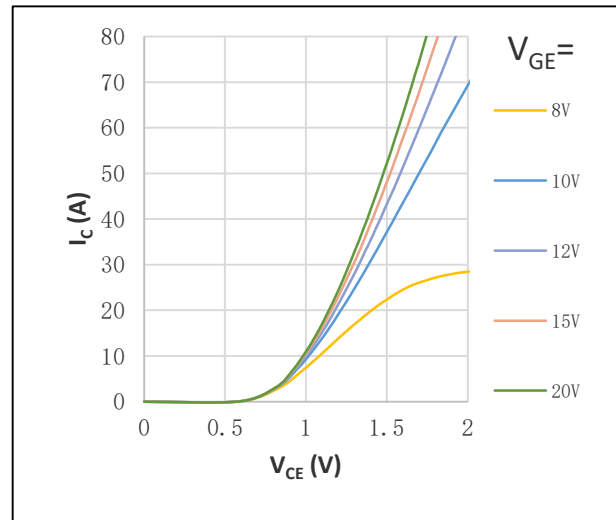
● Fig.2 Input, output and reverse transfer capacitances as a function of collector-emitter voltage; Typical values; $T_j=25^\circ\text{C}$



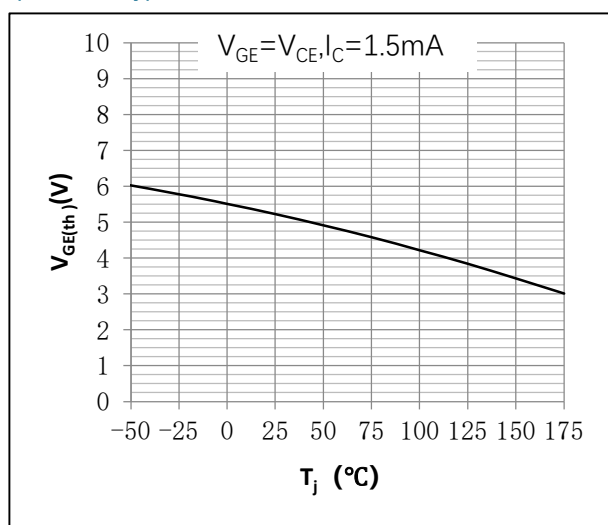
● Fig.3 Output characteristics: collector current as a function of collector-emitter voltage; Typical values; $T_j=25^\circ\text{C}$



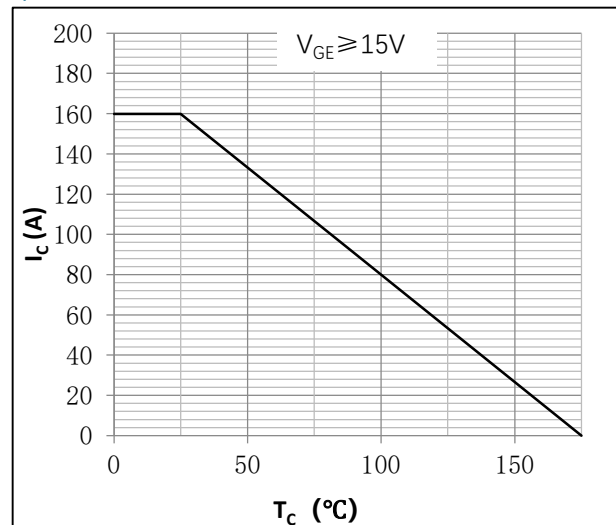
● Fig.4 Output characteristics: collector current as a function of collector-emitter voltage; Typical values: Expanded curve; $T_j=25^\circ\text{C}$



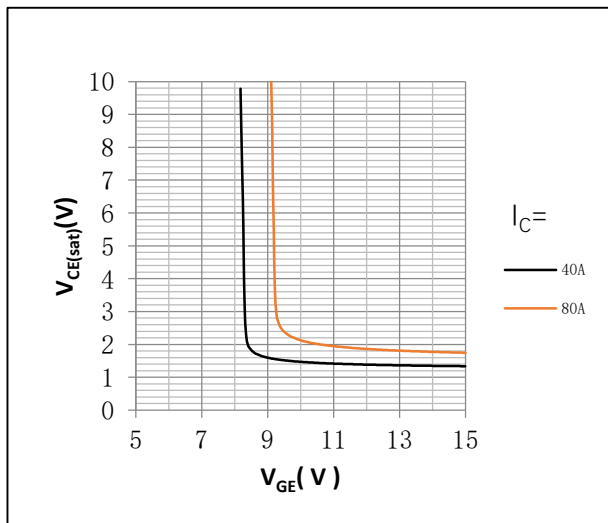
● Fig.5 Gate-emitter threshold voltage as a function of junction temperature; Typical values



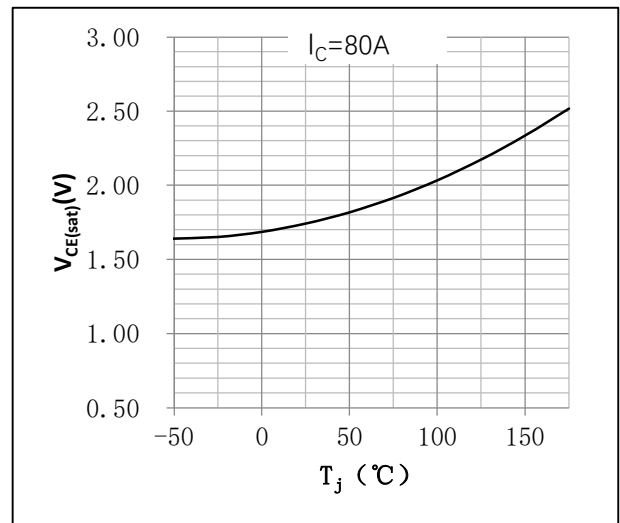
● Fig.6 Continuous collector current as a function of case temperature²; Calculative values



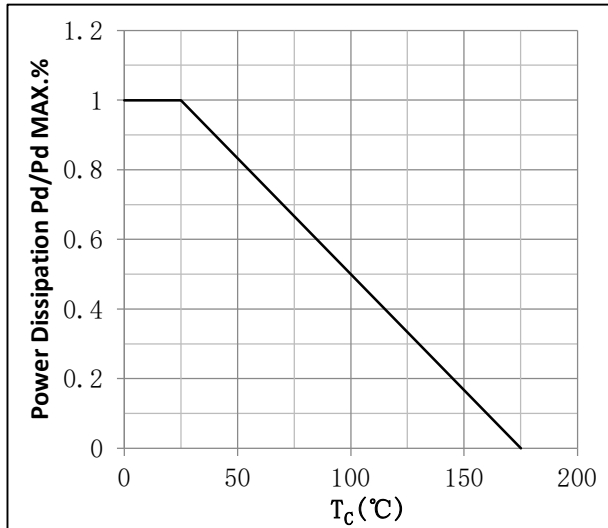
● Fig.7 Collector-emitter saturation voltage as a function of gate-emitter voltage; Typical values



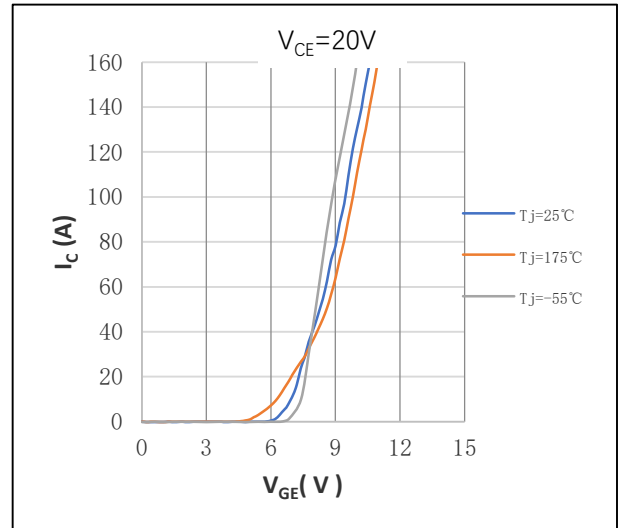
● Fig.8 Fig.6 Collector-emitter saturation voltage as a function of junction temperature; Typical values; $T_j = 25^\circ\text{C}$



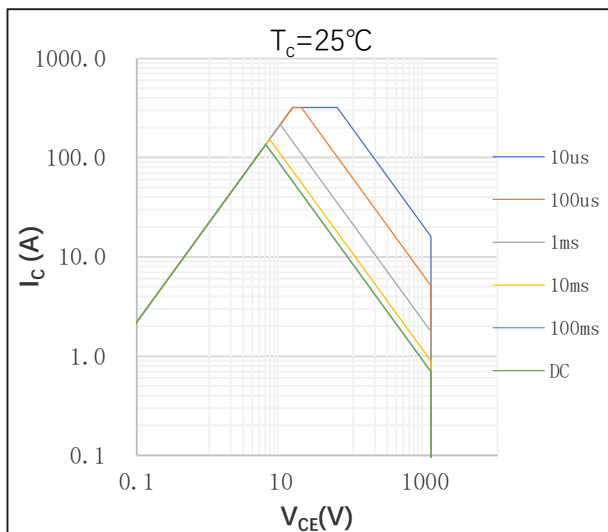
● Figure 9. Normalized total power dissipation as a function of case temperature; Calculative values Normalized Power Dissipation = $P_d/P_d(25^\circ\text{C})$



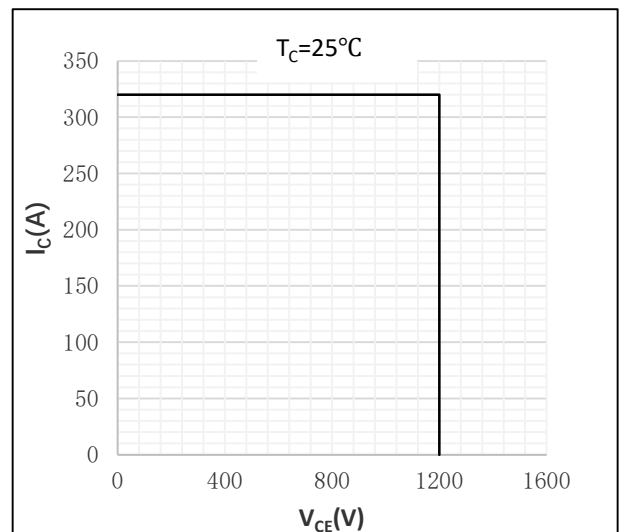
● Figure 10. Transfer characteristics: Collector current as a function of gate-emitter voltage; Typical values



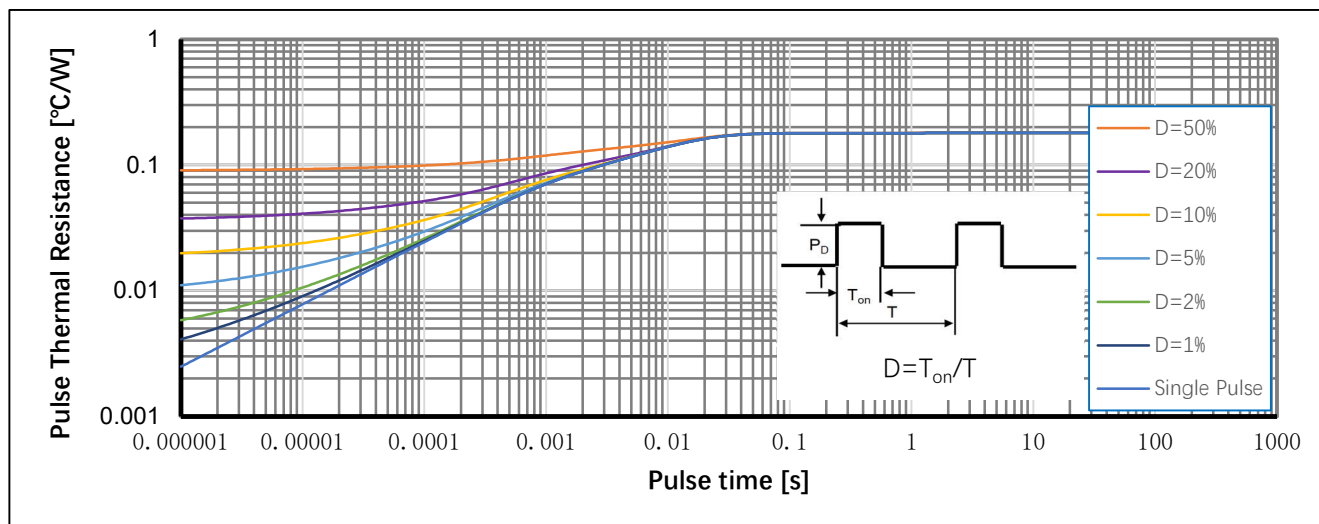
● Fig.11 Safe operating area: continuous and peak collector currents as a function of collector-emitter voltage; Calculative values



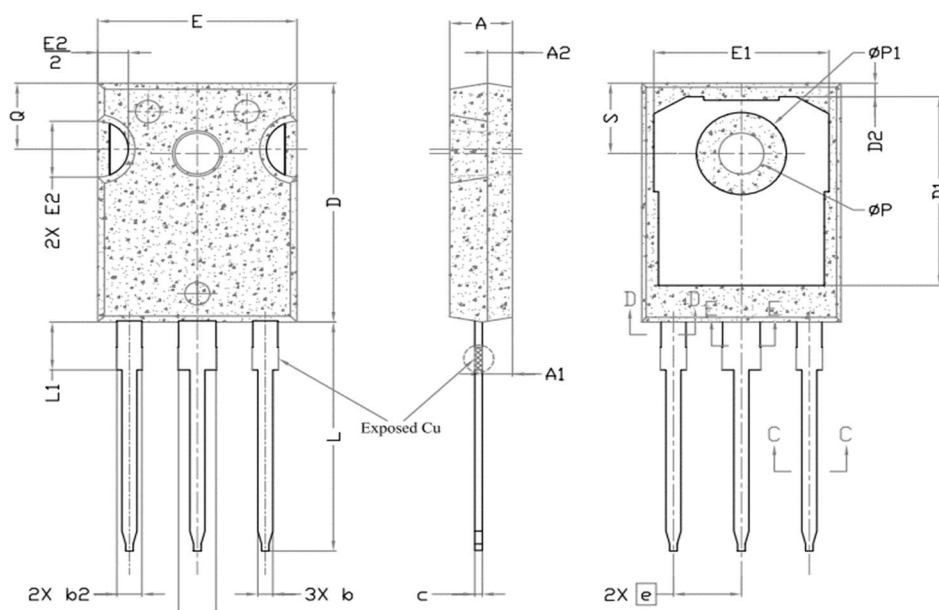
● Fig.12 Reverse bias safe operating area: peak collector currents as a function of collector-emitter voltage; Calculative values



● Fig.13 Transient thermal impedance from junction to case as a function of pulse duration; max values



● Package Outline



SYMBOL	DIMENSIONS			NOTES
	MIN.	NOM.	MAX.	
A	4.83	5.02	5.21	
A1	2.29	2.41	2.55	
A2	1.50	2.00	2.49	
b	1.12	1.20	1.33	
b1	1.12	1.20	1.28	
b2	1.91	2.00	2.39	6
b3	1.91	2.00	2.34	
b4	2.87	3.00	3.22	6, 8
b5	2.87	3.00	3.18	
c	0.55	0.60	0.69	6
c1	0.55	0.60	0.65	
D	20.80	20.95	21.10	4
D1	16.25	16.55	17.65	5
D2	0.51	1.19	1.35	
E	15.75	15.94	16.13	4
E1	13.46	14.02	14.16	5
E2	4.32	4.91	5.49	3
e	5.44BSC			
L	19.81	20.07	20.32	
L1	4.10	4.19	4.40	6
ØP	3.56	3.61	3.65	7
ØP1	7.19REF.			
Q	5.39	5.79	6.20	
S	6.04	6.17	6.30	

● Note

① Pulse : $V_{GE}=+20V/-20V$, Duty cycle=50%, $T_J=175^{\circ}C$, $t=1000$ hours; For DC , the following test conditions can be passed: $V_{GE}=+20V/-10V$, $T_J=175^{\circ}C$, $t=1000$ hours;

② Practically the current will be limited by PCB, thermal design and operating temperature. $V_{GE}=15V$.

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● Revision History

Version	Date	Change
A	2025/8/14	New